



The diagram illustrates a digital baseband processor for a radio receiver. The signal flow is as follows:

- Input 101** splits into two paths:
 - Path 1: **101** → **102** (instantaneous power calculator) → **103** → **104** (compensation coefficient referencing section) → **108** → **110** (non-linear distortion compensating section) → **111** (first orthogonal modulator) → **112** (triangular block) → **113** → **114** → **115** (directional coupler) → **116** (output).
 - Path 2: **101** → **105** → **106** (reference table) → **107** → **108** → **110** (non-linear distortion compensating section).
- Feedback and Control Path:**
 - 115** (directional coupler) → **117** → **118** (phase/amplitude control section) → **119** → **120** (directional coupler).
 - 120** → **125** → **126** (orthogonal demodulator) → **127** → **128** (reference table updating section).
 - 128** ↔ **130** (first data delaying section) → **129** (second data delaying section) → **122** (second orthogonal modulator) → **124** → **120** (directional coupler).
 - 128** ↔ **131** → **130** (first data delaying section).